

EE 2310 Laboratory Project Report Form

A report is required for each EE 2310 laboratory exercise. For each exercise completed, the report should conform to the outline below. A report folder is a nice touch but is not required. The report should have the following components:

- A cover sheet which gives the name of the project, date, and partner names. Be sure to include the classification (CE or EE) of each partner.
- The laboratory instruction sheets (print out from 2310 website).
- Lab exercise item completion list—each item to be initialed by the lab TA (last page of lab instruction sheets).
- The following written sections:
 1. Statement of the learning goal(s). This should be a one- or two-sentence statement that says exactly how you expect to benefit from the laboratory exercise.
 2. Changes to the procedures given in the experiment outline (changes may occasionally be given in the class briefing). If none, section 2 should simply be a statement that there were no changes to the written lab procedure.
 3. Answers to all questions in the laboratory procedure sheets. Answers should be stated clearly and illustrated by expressions, tables, or diagrams as required.
 4. Diagrams of logic circuits assembled or other items which were put together, or, in the case of a program you developed, the specification, flow chart, and assembly language text.
 5. Statements of Results for each part of the lab exercise. These should be clear, coherent paragraphs that encapsulate your results. This should include any data taken, presented in tabular form, together with accompanying truth tables, Boolean expressions, etc., as required.
 6. Discussion of Results. This should be one or more paragraphs discussing the results described in Section 5 and making observations about the exercise. It should include a reconciliation of any discrepancies between results and expected outcomes.
 7. Summary and Conclusions. A summary of your learning experiences, and conclusions about the phenomena studied.

All reports should be neatly typed and printed, and tabular results displayed in an organized manner. Logic circuit diagrams should be laid out using LogicWorks© (other schematic tools may be used – see website for suggestions). Please carefully label all tables, diagrams, or software source text, or other required information, so that it is identified accurately and clearly. Each partnership group will submit a single report, but partners are expected to jointly participate in creating the report, including plots and other tabular data. Team members are also expected to equally participate in each project activity, including building circuits and taking data, as well as writing the report. Reports are due according to the homework due table. Note that reports are not required for some software development labs.